



DEV BHOOMI INSTITUTE OF TECHNOLOGY

DEHRADUN, UTTARAKHAND

Event organized Report (Academic Year: 2015-16)

Event Title:	Workshop on VLSI & VHDL	
Event category (Workshop/Guest Lecture/Seminar/Education Visit)	One day Workshop	
Objective of the Event	The objective of the VLSI Circuits and Technology Committee is to identify new/hot areas of interest to the Electron Device and Solid-State Circuits communities. VHDL stands for very high-speed integrated circuit hardware description language. It is a programming language used to model a digital system by dataflow, behavioral and structural style of modeling.	
Outcome of the Event	Students learned to concepts of VLSI design flow of digital systems and various tools used for VLSI circuit design are discussed.	
Resource Person/ Expert and Organization	NAME	
	Organization	Industry trainer
Number of Participants	38 (ECE)	
Date & Venue of Events:	22/09/2015 and venue is CSE lab, DBIT main building	
Event Coordinator	Mr. Tej Raj	



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PHOTO GALLERY

